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CPL 10/26/06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hui Zhang et al.

Practitioner's Docket No.: 1856

Filing Date: 01/29/2004

For: Buffer Bypass Circuit for Reducing  
Latency in Information Transfers to a Bus

Serial No.: 10/767,001

Examiner: Lee, Christopher E.

Art Unit: 2112

Confirmation No. 5019

Mail Stop Amendment  
Commissioner for Patent  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Examiner's Action mailed on March 15, 2006, the above-identified patent application is amended a second time as follows. Applicants have elected to present the amendment using the revised amendment format set forth in the waiver of 37 CFR 1.121.

AMENDMENT AF

(37 C.F.R. § 1.111)

**Amendments to the claims** begin of page 2 of this paper.

**Remarks** begin on page 6 of this paper.